SIM-V for RISC-V Customization

Customize, Simulate, Analyze, Optimize

➢ Custom instructions, registers, CSRs and more

➢ RTOS, Linux and Android starting points

Explore the future of RISC-V CPU customization through SIM-V, MachineWare's cutting edge RISC-V simulation technology. Simulate complete RISC-V systems and deploy complex software stacks without any manual intervention.

The **SIM-V Extension SDK** empowers architects to seamlessly incorporate their unique extensions into SIM-V, facilitating the integration of custom instructions, registers, CSRs, and more for system level simulation.

Jumpstart your journey by leveraging MachineWare's RISC-V Reference Model, offering **RTOS**, **Android**, or **Linux** starting points to accelerate your customization process.

Get in-depth target software execution statistics, enabling hotspot analysis for immediate feedback on your customization design choices.

Customize

Optimize

Analyze   **Simulate**

SIM-V

MachineWare RISC-V Reference Model

Custom Extension

SIM-V Extension API

Simulation Engine

Simulation Host

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